

INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: LEAD FRAME, SEMICONDUCTOR CHIP PACKAGE,
Title Line Two:: METHOD FOR MANUFACTURING SEMICONDUCTOR D
Title Line Three:: EVICE, AND SEMICONDUCTOR DEVICE
Total Drawing Sheets:: 18
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: 9319S-000666
Secrecy Order in Parent Appl.?: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 27572

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2003-044360
Filing Date:: 02-21-2003
Country:: Japan
Priority Claimed:: Yes

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